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SIDDHARTH INSTITUTE OF ENGINEERING & TECHNOLOGY:: PUTTUR
(AUTONOMOUS)

B.Tech III Year II Semester Supplementary Examinations July-2021

OBJECT ORIENTED ANALYSIS AND DESIGN

(Common to CSE & CSIT)

Time: 3 hours

Max. Marks: 60

(Answer all Five Units **5 x 12 = 60** Marks)

UNIT-I

- 1 a Categorize the software development life cycle in detail 6M
b List out the principles of modeling in detail and explain it. 6M

OR

- 2 a Explain the relationships in the UML 6M
b Compare and evaluate the behavioural things in the model 6M

UNIT-II

- 3 a Explain Stereotypes, tagged values, and constraints with examples. 12M

OR

- 4 a Explain the Interfaces, types, roles, and realization. 8M
b Explain Modeling of Non software things. 4M

UNIT-III

- 5 a Explain in details of Use cases, actors, include, and extend with suitable example. 7M
b Design a use case diagram to show ATM system. 5M

OR

- 6 a Explain Roles, links, messages, actions, and sequences of interactions. 6M
b Explain sequence diagram with an example. 6M

UNIT-IV

- 7 Explain the common modeling techniques for state chart diagrams with suitable example. 12M

OR

- 8 a List the term and concepts of processes and threads. 6M
b List the Common modeling techniques for processes and threads. 6M

UNIT-V

- 9 a Elaborate Modeling an API. 6M
b Design a component diagram for hostel management. 6M

OR

- 10 a Apply the technique behind Modeling source code and explain. 6M
b Explain Mapping between logical and physical models. 6M

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